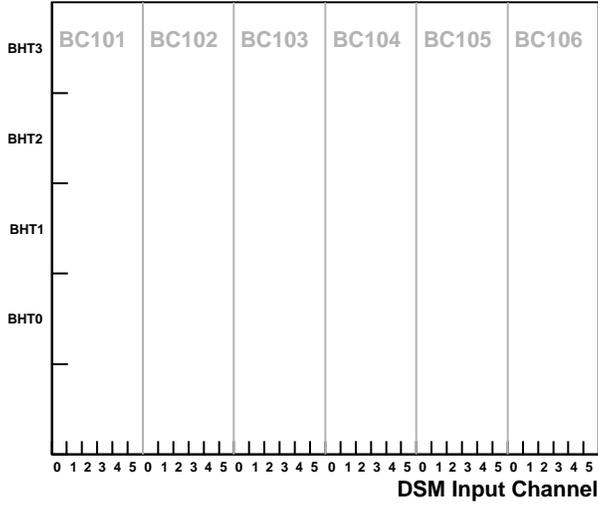


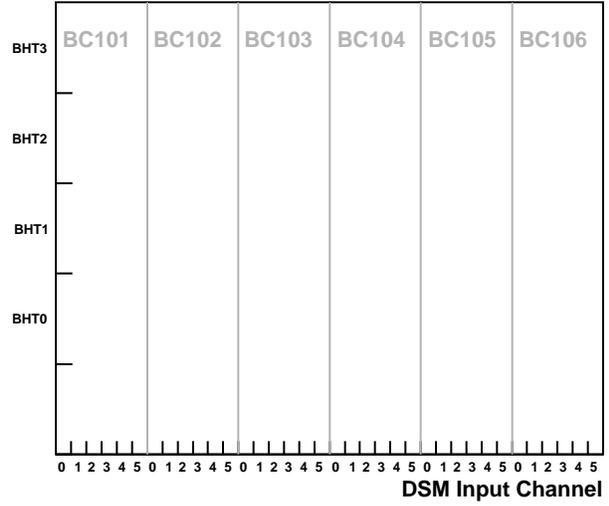
**Barrel EMC L1 Input - High Tower Bits**

Entries 0



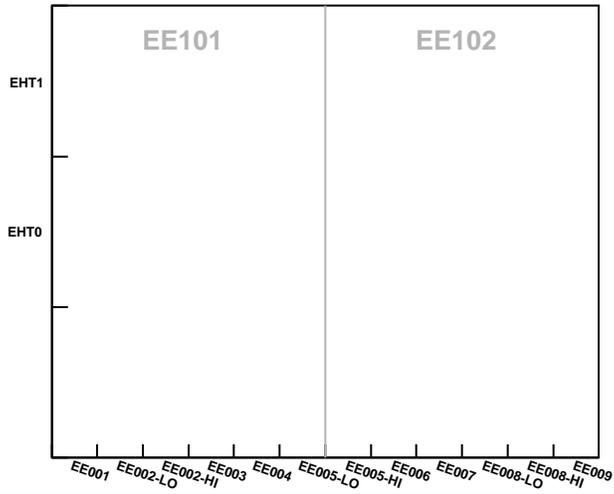
**Barrel EMC L1 Input - High Tower Bits Errors**

Entries 0



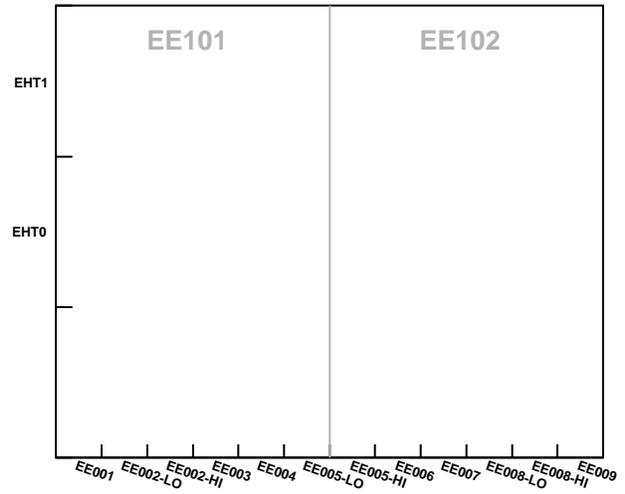
**Endcap EMC L1 Input - High Tower Bits**

Entries 0



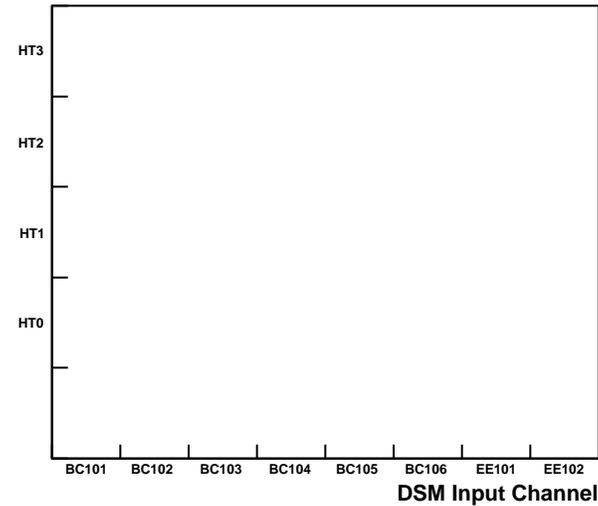
**Endcap EMC L1 Input - High Tower Bits Errors**

Entries 0



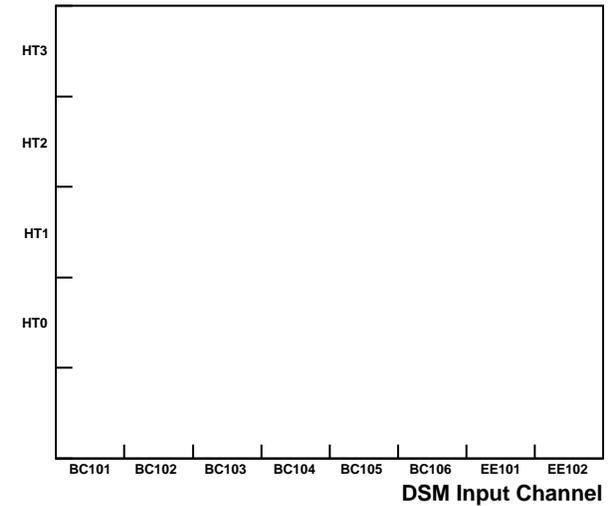
**EMC L2 Input - High Tower Bits**

Entries 0



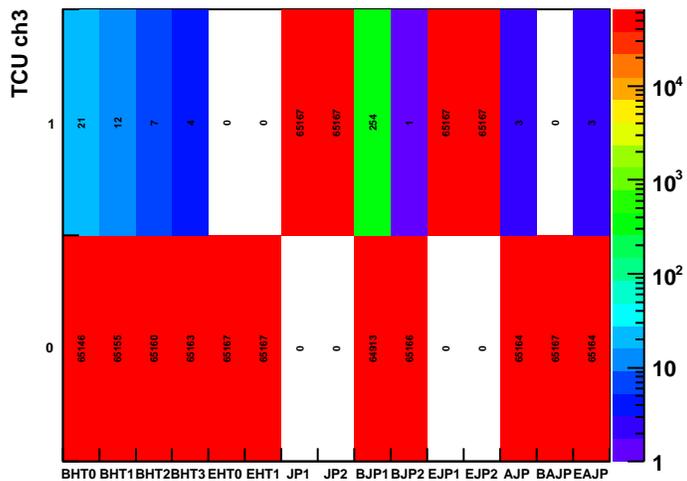
**EMC L2 Input - High Tower Bits Errors**

Entries 0



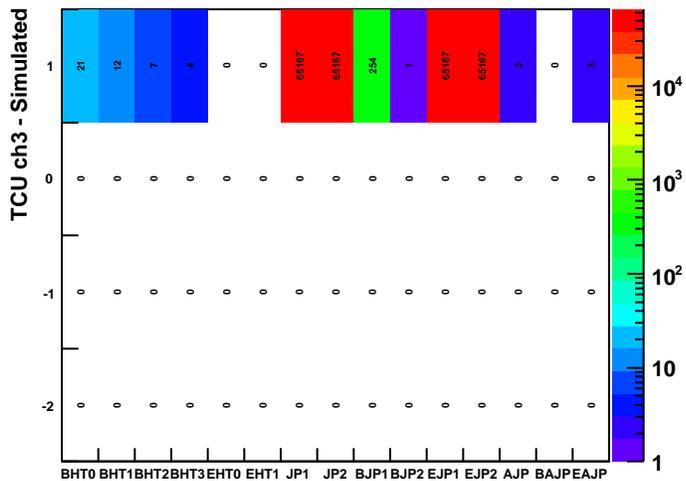
TCU ch3 Input (EM201)

Entries 977505



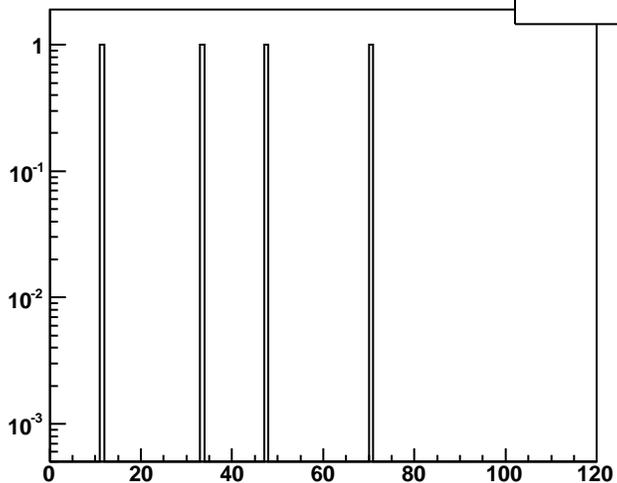
TCU ch3 Input (EM201)

Entries 260973



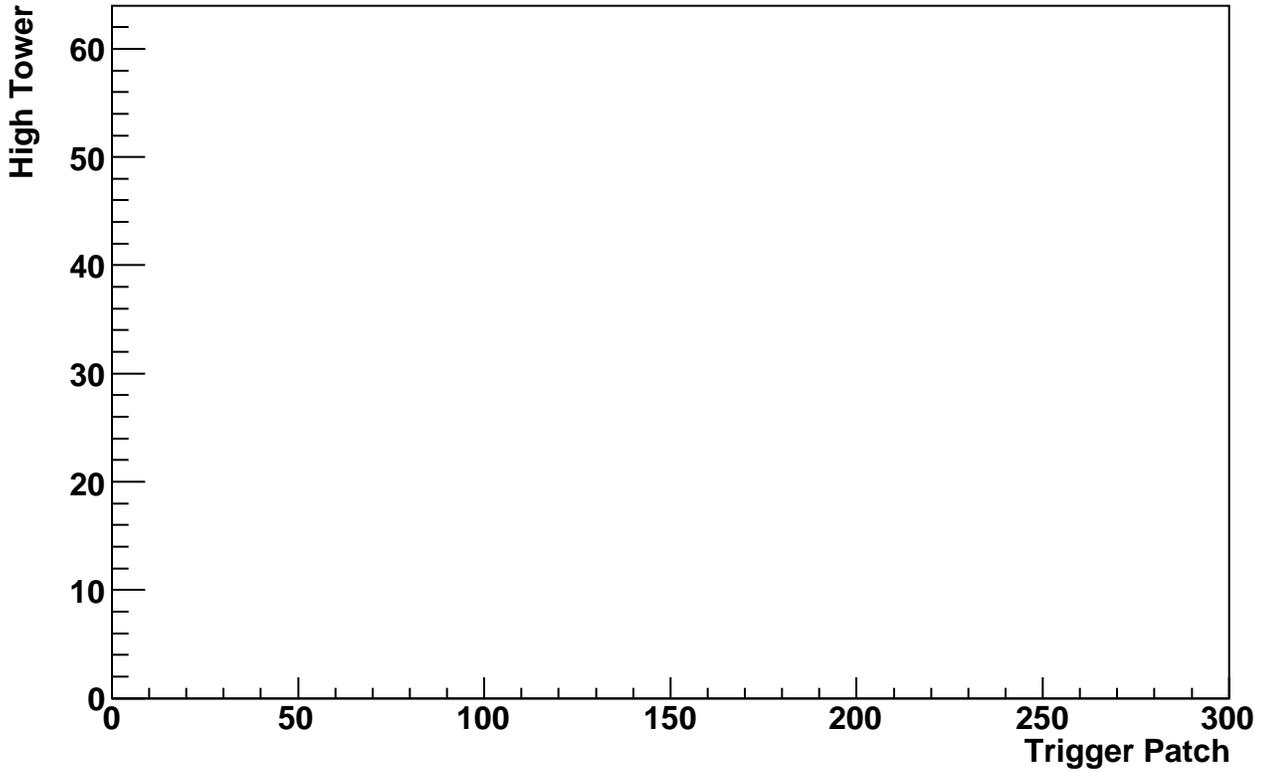
BunchId7Bit (BHT3)

Entries 4



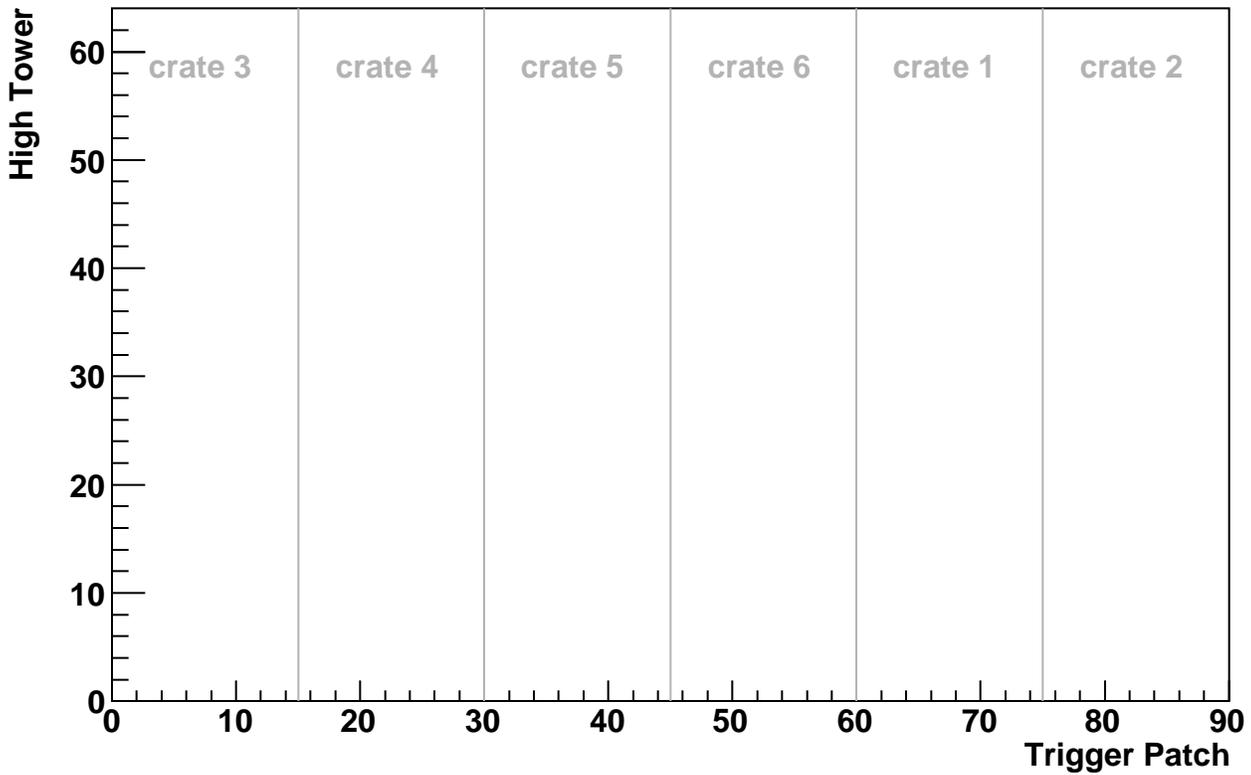
**Barrel EMC L0 Input - High Tower**

Entries 0



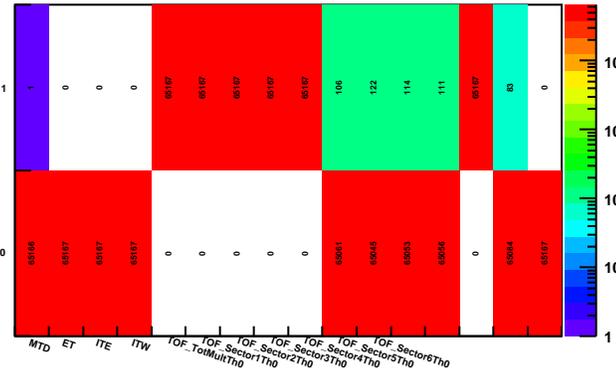
**Endcap EMC L0 Input - High Tower**

Entries 0



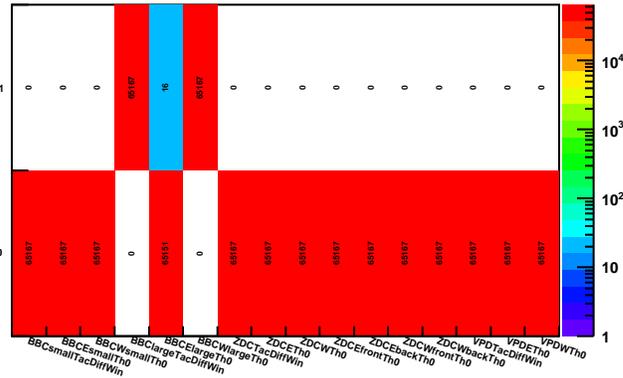
TF201 0-15 (ch0)

Entries 1042672



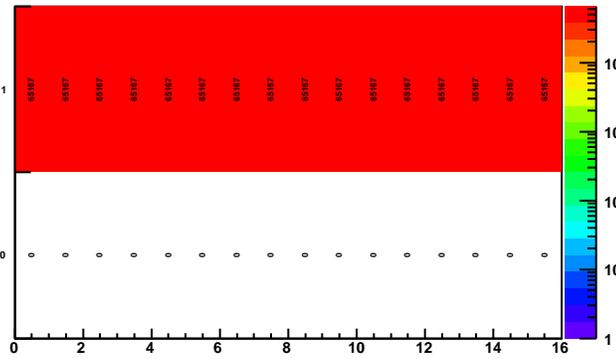
VT201 0-15 (ch1)

Entries 1042672



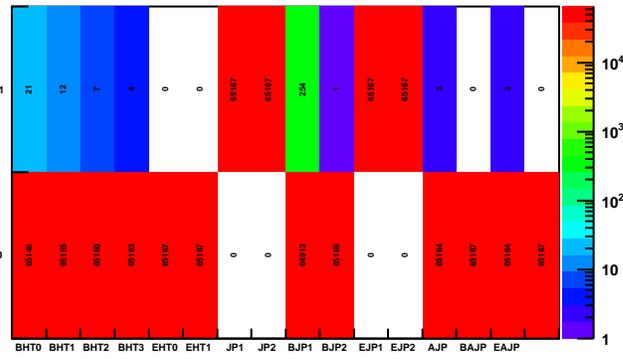
Unused (ch2)

Entries 1042672



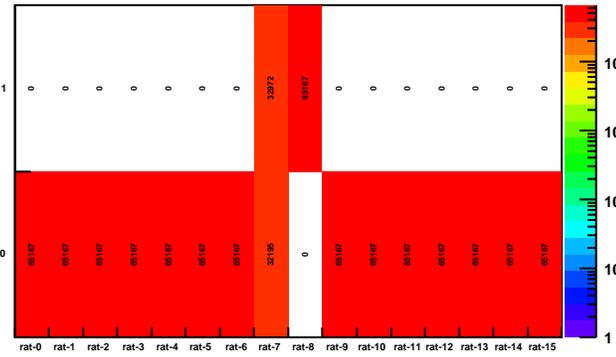
EM201 0-15 (ch3)

Entries 1042672



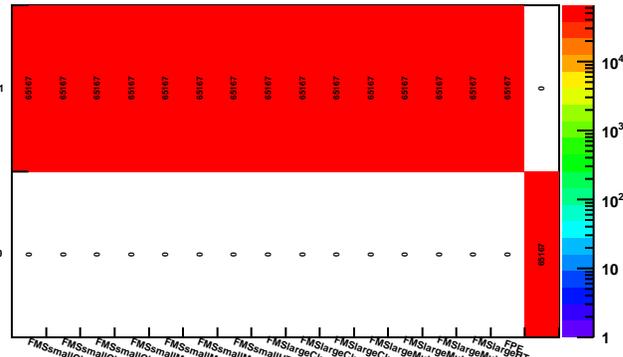
RAT board (ch4)

Entries 1042672



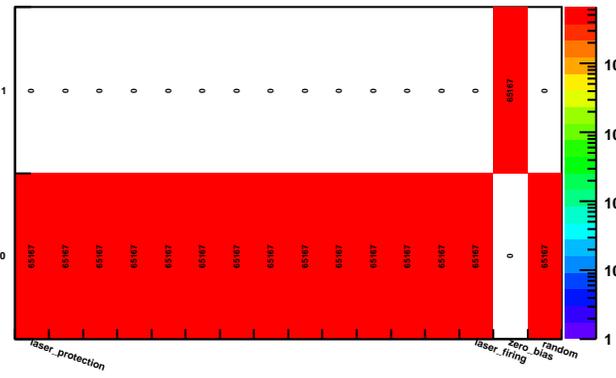
FP201 0-15 (ch5)

Entries 1042672



ST201 0-15 (ch6)

Entries 1042672



Unused (ch7)

Entries 1042672

